

DD4
10-6-06

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	44	bit with (first near2 configur\$5) with (second near2 configur\$5) and "712"/\$3?.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L2	14	array with (first near2 configur\$5) with (second near2 configur\$5) and "712"/\$3?.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L3	21	parady.in. and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L4	1	"4763242".pn.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L5	5	bit with (first near2 context) with (second near2 context) and "712"/\$3?.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L6	221	bit with reconfigur\$5 and "712"/\$3?.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L7	1	"5890008".pn.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L8	392	(status state) near2 register with (sleep nap idle) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L9	60	(status state) near2 register with processor with (sleep nap idle) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L10	5	(mode) near2 register with processor with (sleep nap idle) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L11	13	(status state) near2 register with processor with (sleep nap idle) with (active busy) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L12	52	VLIW with thread and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L13	205	712/10.ccls.	US-PGPUB; USPAT	OR	OFF	2006/10/06 14:15
L14	396	712/11.ccls.	US-PGPUB; USPAT	OR	OFF	2006/10/06 14:15
L15	220	712/15.ccls.	US-PGPUB; USPAT	OR	OFF	2006/10/06 14:15
L16	623	712/228.ccls.	US-PGPUB; USPAT	OR	OFF	2006/10/06 14:15
L17	4	(multiplexer selector) with (plurality multiple) with (register adj (file set)) same context	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L18	72	(multiplexer selector) with (register adj (file set)) same context	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15

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L19	205	712/10.ccls.	US-PGPUB; USPAT	OR	OFF	2006/10/06 14:15
L20	220	712/15.ccls.	US-PGPUB; USPAT	OR	OFF	2006/10/06 14:15
L21	396	712/11.ccls.	US-PGPUB; USPAT	OR	OFF	2006/10/06 14:15
L22	623	712/228.ccls.	US-PGPUB; USPAT	OR	OFF	2006/10/06 14:15
L23	44	bit with (first near2 configur\$5) with (second near2 configur\$5) and "712"/\$3?.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L24	277	(array matrix) same (first near2 configur\$5) same (second near2 configur\$5) same process\$3	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L25	19	(array matrix) same (first near2 configur\$5) same (second near2 configur\$5) same process\$3 same (row and column)	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L26	16	(array matrix) same (first near2 configur\$5) same (second near2 configur\$5) same process\$3 and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L27	1	(array matrix) same (first near2 configur\$5) same (second near2 configur\$5) same (row column) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L28	0	(array matrix) same \$2configur\$5 with different with number with (row column) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L29	5	(array matrix) same \$2configur\$5 with different with (row column) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L30	1	(array matrix) same \$2configur\$5 with different\$2 with organiz\$5 and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L31	23	(array matrix) with \$2configur\$5 with organiz\$5 and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L32	0	(array matrix) same (("3x3" "3 x 3" "3 by 3") and ("2 x 2" "2 by 2" "2x2")) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L33	14	(array matrix) same reconfigur\$5 same (flag bit) near4 (set setting) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L34	9	(li gifford).in. and SIMD and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15

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L35	39	bit with (enable disable) with (process\$3 near3 element) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L36	248	(array matrix) and (1x? ?x1) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L37	14	(array matrix) same (1x? ?x1) same (disabl\$3 enabl\$3) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L38	0	(array matrix) same ("1 x ?" "? x 1") same (disabl\$3 enabl\$3) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L39	0	(array matrix) near4 (1x? ?x1) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L40	617	bit with (disabl\$3 enabl\$3) with (processor coprocessor) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L41	69	bit near3 (disabl\$3 enabl\$3) near3 ((process\$3 near3 element) PE) and "712"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L42	7	(US-4783738-\$ or US-5045995-\$ or US-4873626-\$ or US-4891787-\$ or US-5243699-\$ or US-5542074-\$ or US-5727229-\$).did.	USPAT	OR	ON	2006/10/06 14:15
L43	0	(US-4783738-\$ or US-5045995-\$ or US-4873626-\$ or US-4891787-\$ or US-5243699-\$ or US-5542074-\$ or US-5727229-\$).did. and "register file" with each	USPAT	OR	ON	2006/10/06 14:15
L44	2	(US-4783738-\$ or US-5045995-\$ or US-4873626-\$ or US-4891787-\$ or US-5243699-\$ or US-5542074-\$ or US-5727229-\$).did. and "register file"	USPAT	OR	ON	2006/10/06 14:15
L45	1	"4314349".pn.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L46	851	(process task thread) same (ID identifier) same (physical and virtual)	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L47	618	(process task thread) same (ID identifier) same (physical and virtual) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L48	118	(process task thread) same (ID identifier) same (physical and virtual) and (PE (process\$3 near2 element)) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L49	20	(process task thread) same (ID identifier) near4 (physical and virtual) and (PE (process\$3 near2 element)) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15

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L50	117	vliw with simd with (type form)	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L51	813	instruction with virtual with (ID identifi\$6)	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L52	310	instruction with virtual near2 (ID identifi\$6)	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L53	90	instruction near3 virtual near2 (ID identifi\$6)	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L54	22	(ID identifier) with (physical and virtual) same (PE (process\$3 near2 element)) and g06f\$.ipc.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L55	2	"virtual processor is"	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L56	19	"virtual processor" with (advantag\$3 benefi\$4)	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15
L57	1	"6167501".pn.	US-PGPUB; USPAT	OR	ON	2006/10/06 14:15